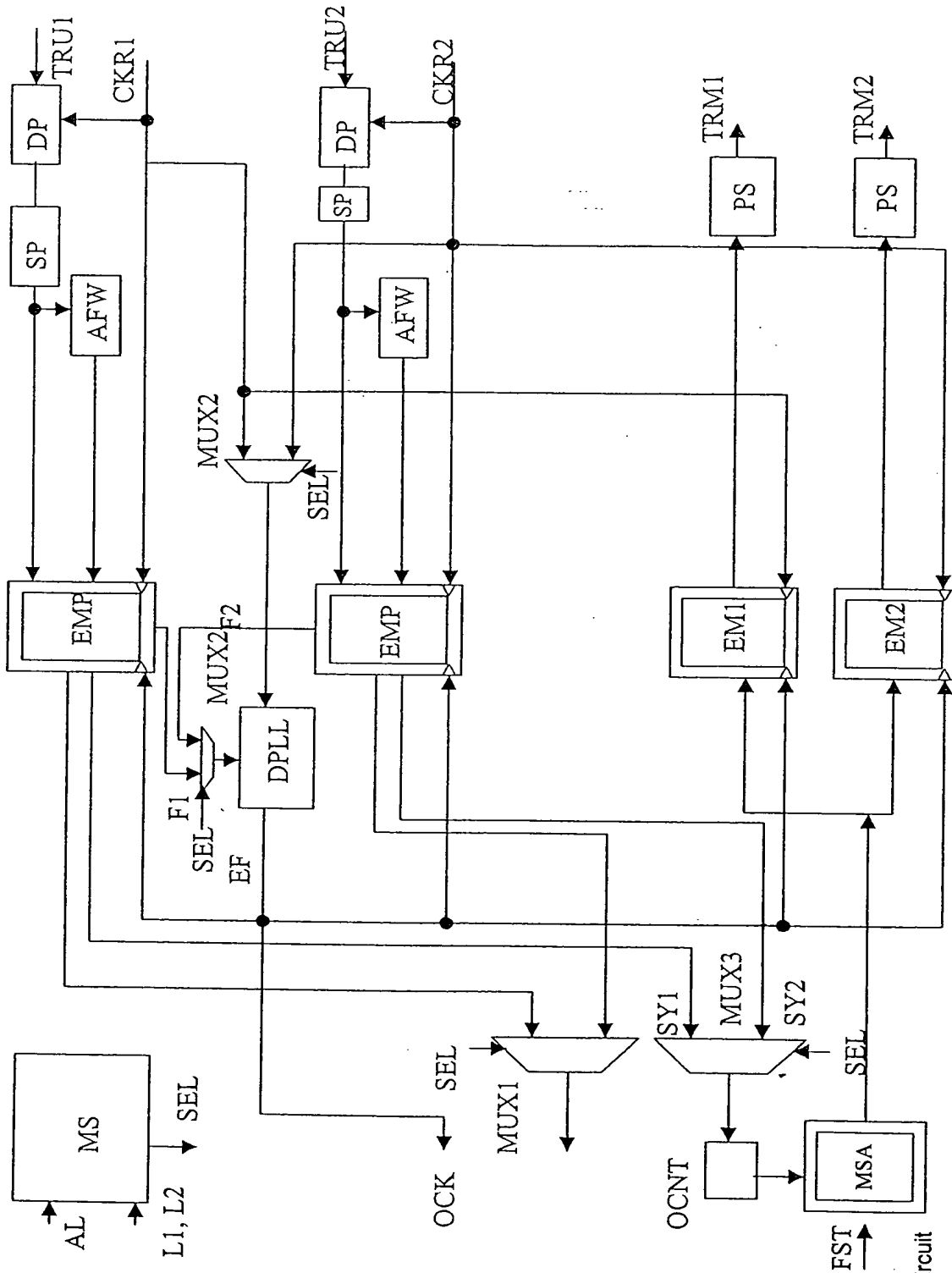
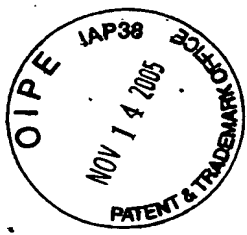


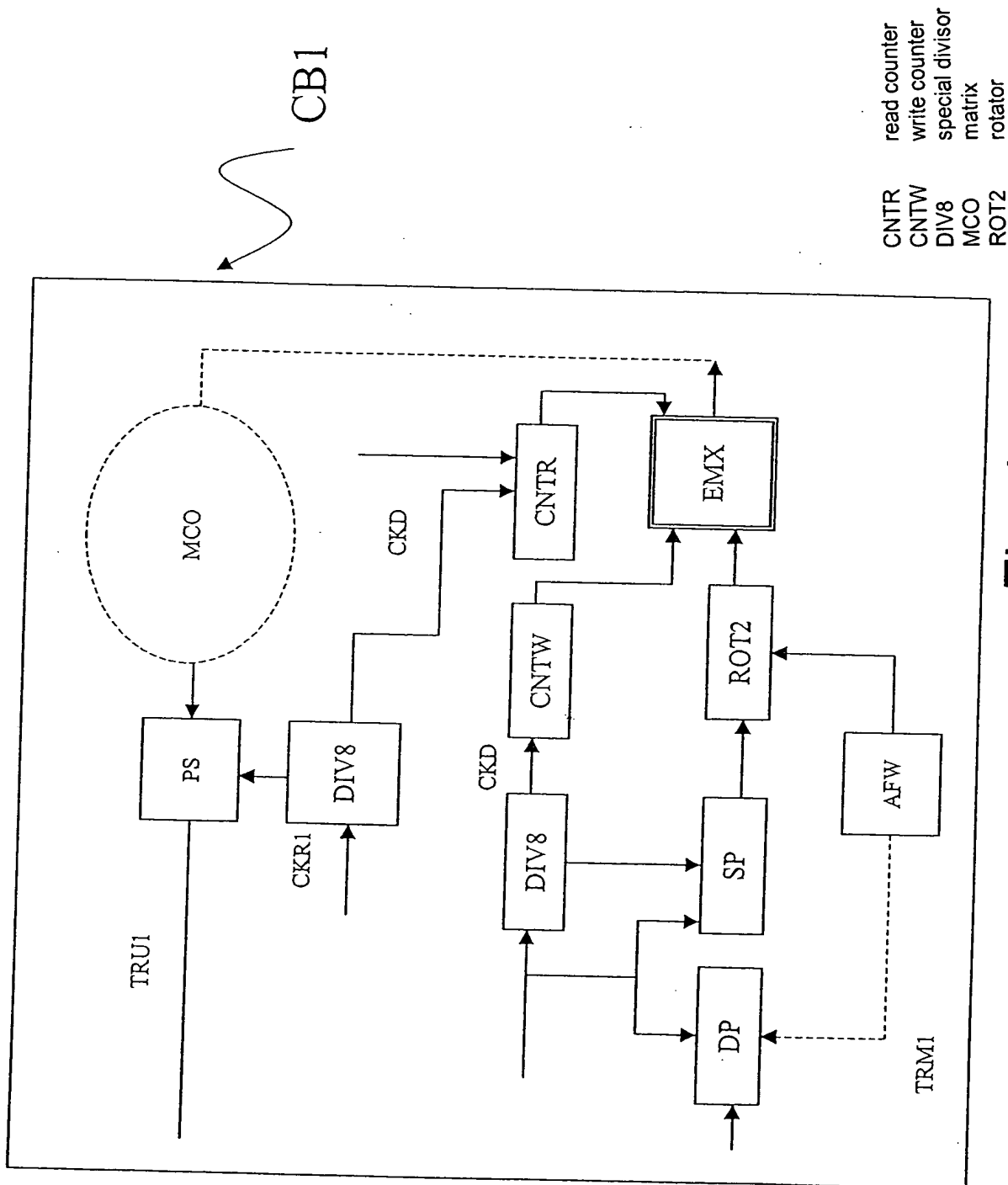
**Fig. 1**



**Fig. 2**

AFW frame aligner circuit  
MS state machine  
MSA input memory block  
OCNT offset counter  
PS parallel-serial converter device  
SP serial-parallel converter device





read counter  
 write counter  
 special divisor  
 matrix  
 rotator

CNTR  
 CNTW  
 DIV8  
 MCO  
 ROT2

**Fig. 4**